## 1. General description

Bidirectional ElectroStatic Discharge (ESD) protection diode in a very small SOD323 (SC-76) SMD plastic package designed to protect one signal line from the damage caused by ESD and other transients.

## 2. Features and benefits

- · Bidirectional ESD protection of one line
- Max. peak pulse power: P<sub>ppm</sub> = 500 W
- Low clamping voltage: V<sub>CL</sub> = 26 V
- Ultra low leakage current: I<sub>RM</sub> = 0.09 μA
- ESD protection up to 30 kV
- IEC 61000-4-2, level 4 (ESD)
- IEC 61000-4-5 (surge); I<sub>PPM</sub> = 18 A
- Very small SMD plastic package

## 3. Applications

- · Computers and peripherals
- · Communication systems
- · Audio and video equipment
- Data lines

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	3.3	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	101	-	pF



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	1 2	[54 51]
2	K2	cathode (diode 2)		K1   K2
			SOD323	sym045

## 6. Ordering information

#### **Table 3. Ordering information**

Type number		Package					
		Name	Description	Version			
	PESD3V3L1BA		plastic, surface-mounted package; 2 leads; 1.3 mm pitch; 1.7 mm x 1.25 mm x 0.95 mm body	SOD323			

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PESD3V3L1BA	АВ

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P <sub>PPM</sub>	rated peak pulse power	t <sub>p</sub> = 8/20 μs	[1]	-	500	W
I <sub>PPM</sub>	rated peak pulse current		[1]	-	18	Α
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximu	um ratings			'		
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2; contact discharge; T <sub>amb</sub> = 25 °C	[2]	-	30	kV
		IEC 61000-4-2; air discharge		-	15	kV
		MIL-STD-883; human body model (HBM); T <sub>amb</sub> = 25 °C		-	10	kV

- [1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Device stressed with ten non-repetitive ESD pulses.

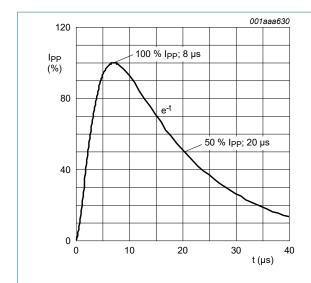


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5

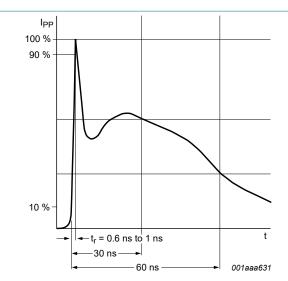


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

**Table 6. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	3.3	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C		5.8	6.4	6.9	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 3.3 V; T <sub>amb</sub> = 25 °C		-	0.09	2	μA
C <sub>d</sub>	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$		-	101	-	pF
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; T <sub>amb</sub> = 25 °C	[1]	-	-	8	V
		I <sub>PPM</sub> = 18 A; T <sub>amb</sub> = 25 °C	[1]	-	-	26	V
R <sub>diff</sub>	differential resistance	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C		-	-	400	Ω

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

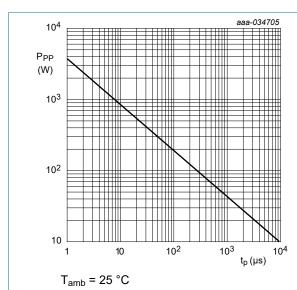


Fig. 3. Peak pulse power as a function of exponential pulse duration; typical values

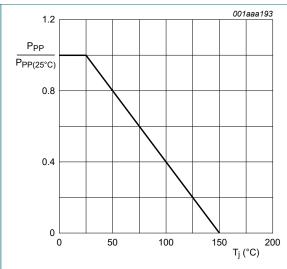


Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

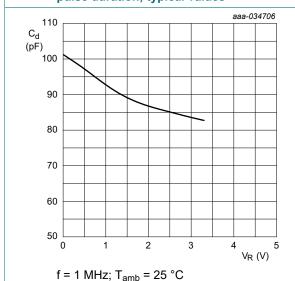


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

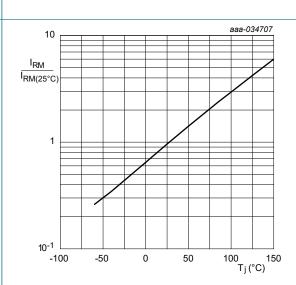
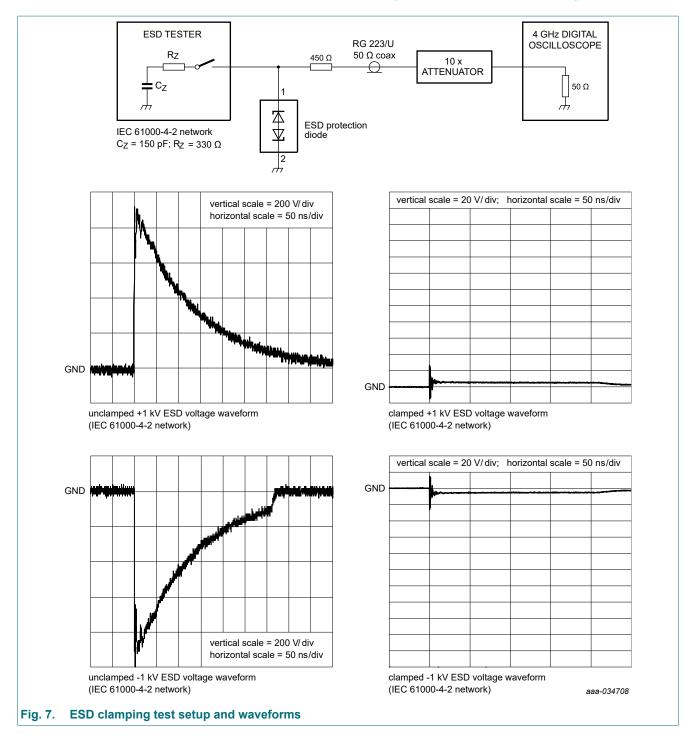


Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values

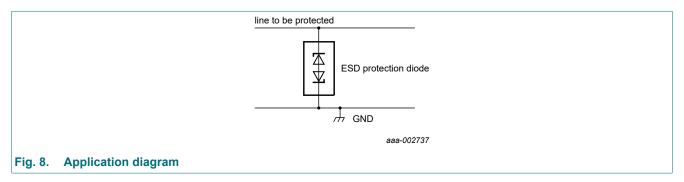
Nexperia PESD3V3L1BA

### Low capacitance bidirectional ESD protection diode



## 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

6/11

## 11. Package outline

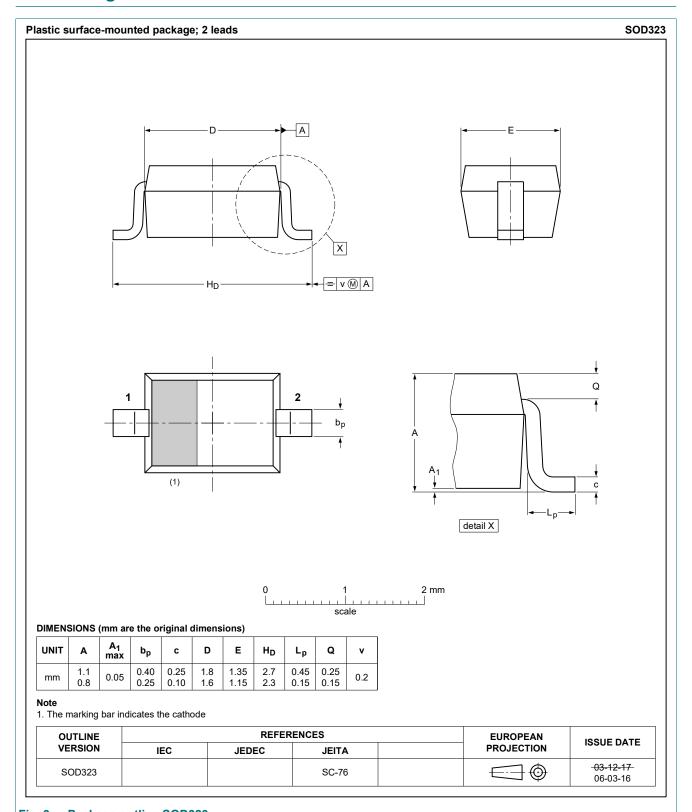
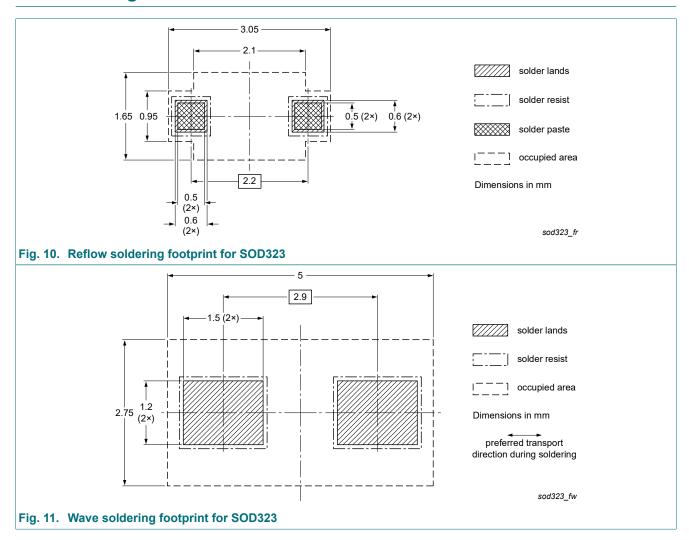


Fig. 9. Package outline SOD323

## 12. Soldering



# 13. Revision history

#### **Table 7. Revision history**

Tubio 1. Itoviololi illoto	• )			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD3V3L1BA v.3	20230411	Product data sheet	-	PESDXL1BA_SER_2
Modifications:	<ul> <li>Family data sheet reduced to single type data sheet.</li> <li>Product changed to non-automotive qualification. Please refer to nexperia.c (-Q) product alternative(s).</li> </ul>		eria.com for automotive	
PESDXL1BA_SER_2	20090820	Product data sheet	-	PESDXL1BA_SER_1
PESDXL1BA_SER_1	20041004	Product data sheet	-	-

## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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